

FIG. 1

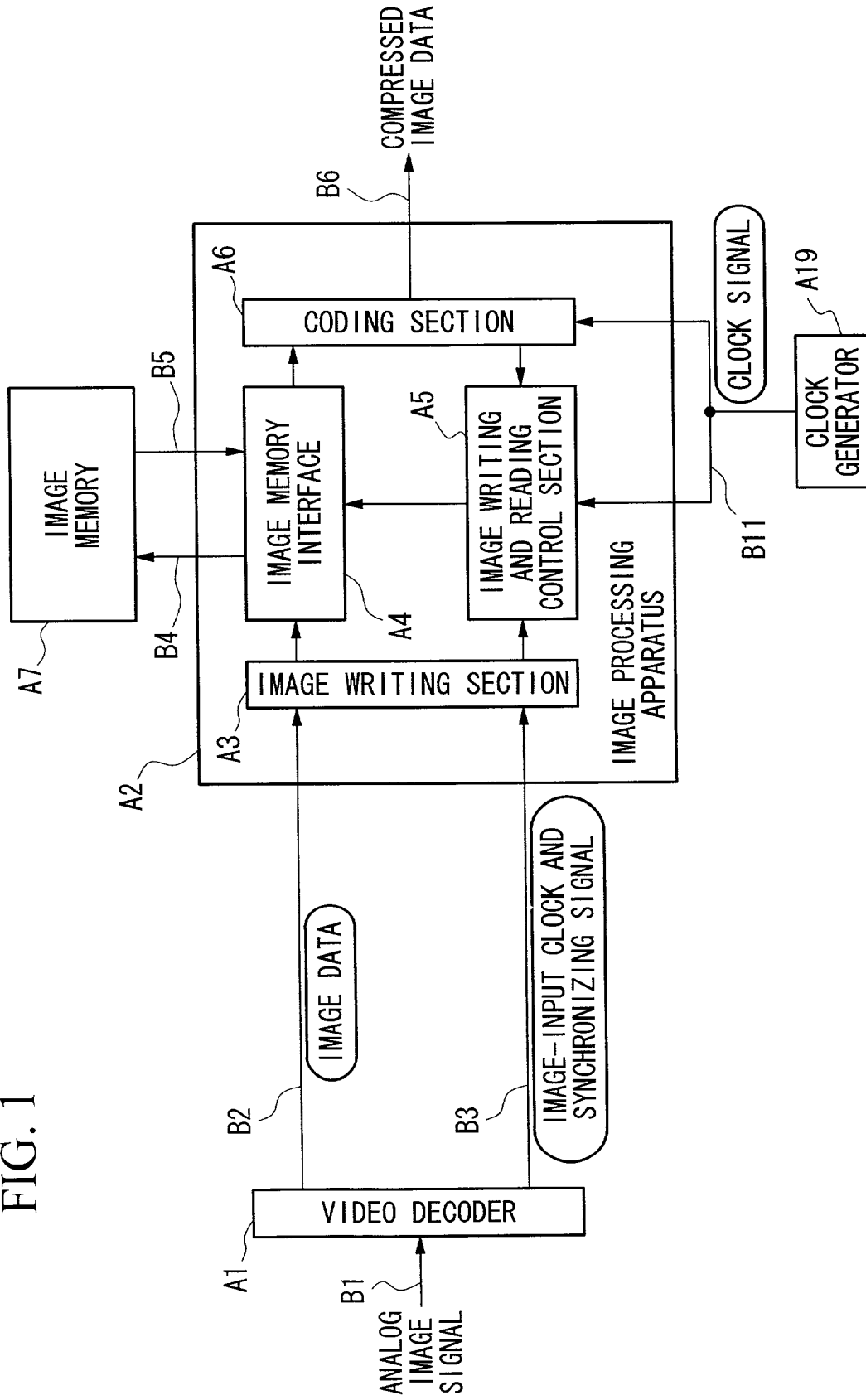


FIG. 2

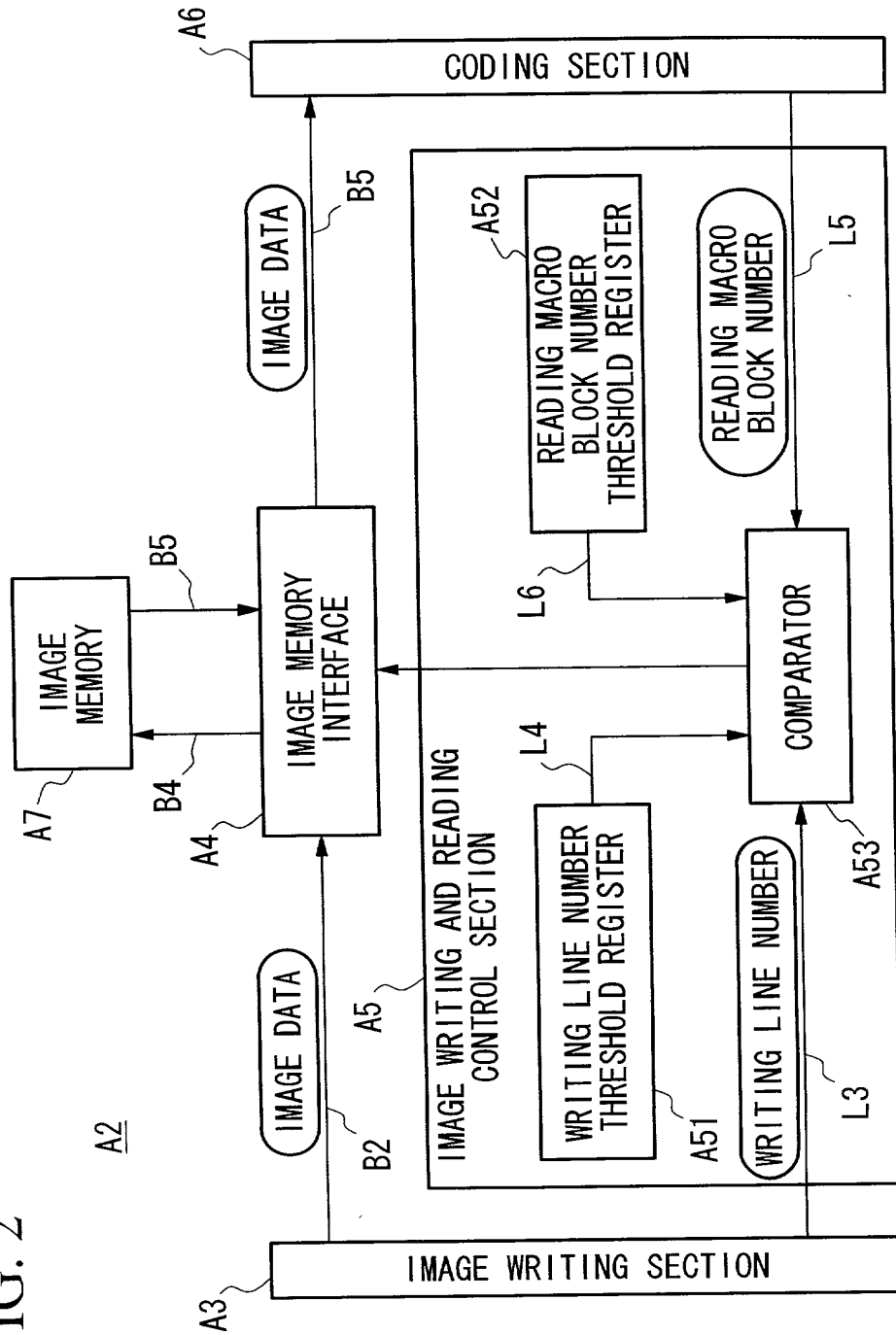


FIG. 3A

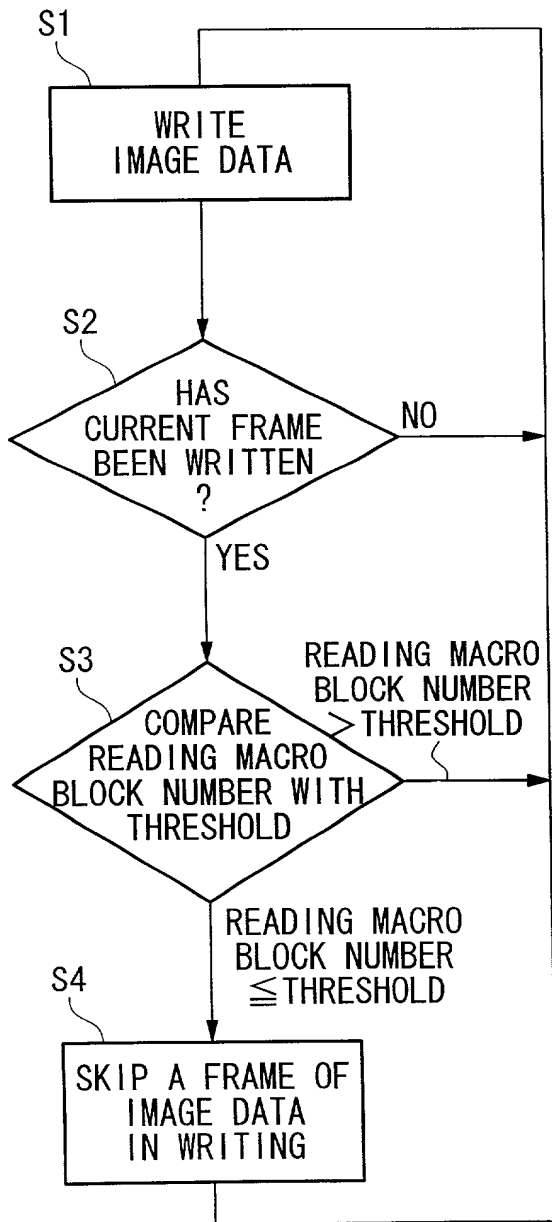
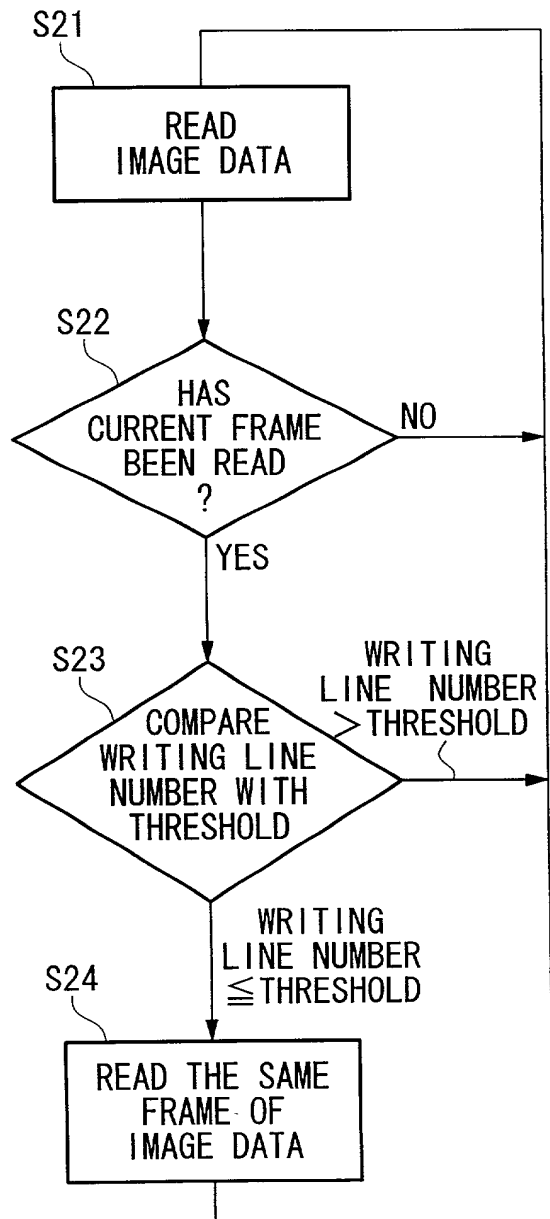


FIG. 3B



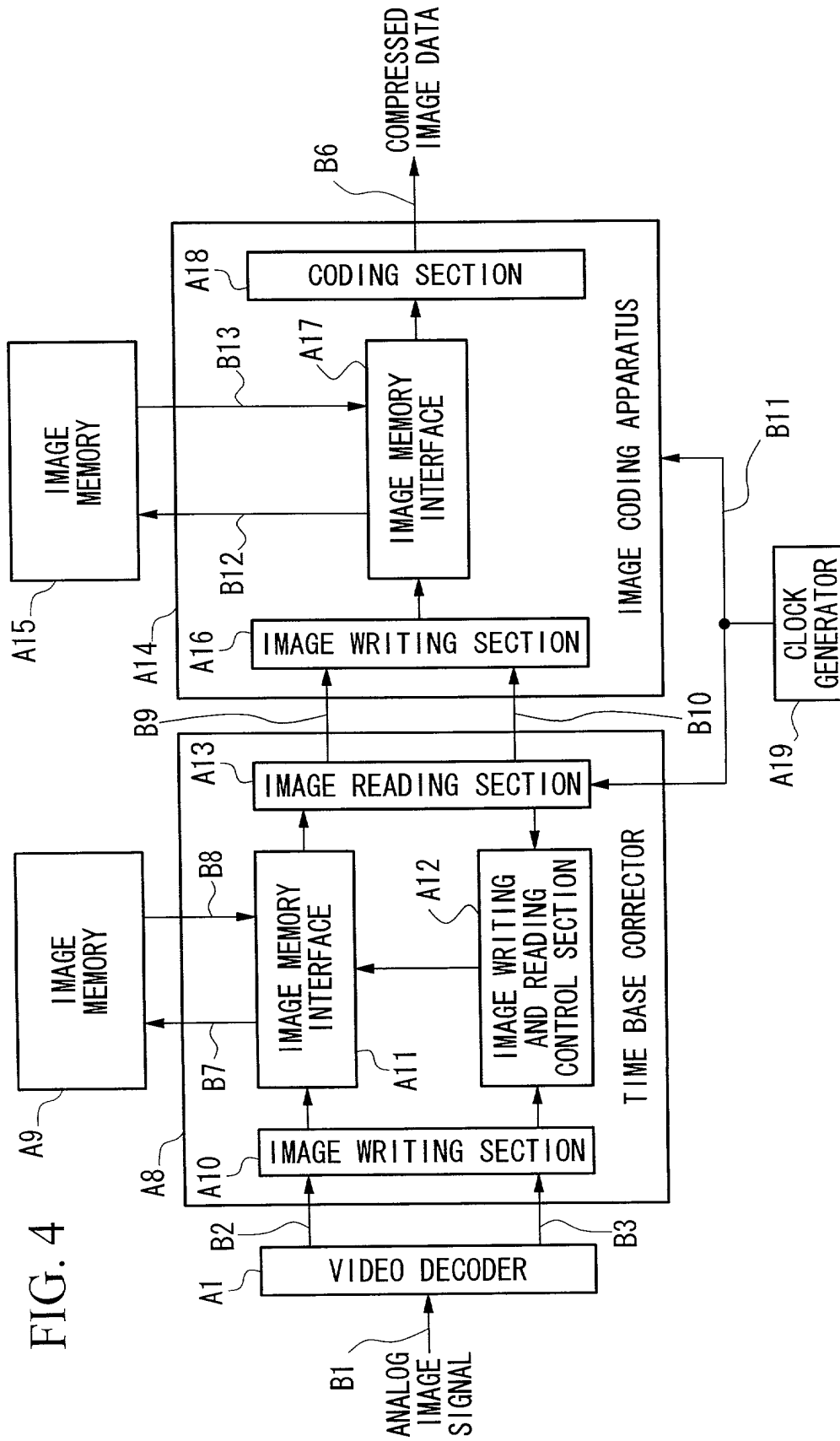


FIG. 4

FIG. 4 is a block diagram of an image processing apparatus.

FIG. 5

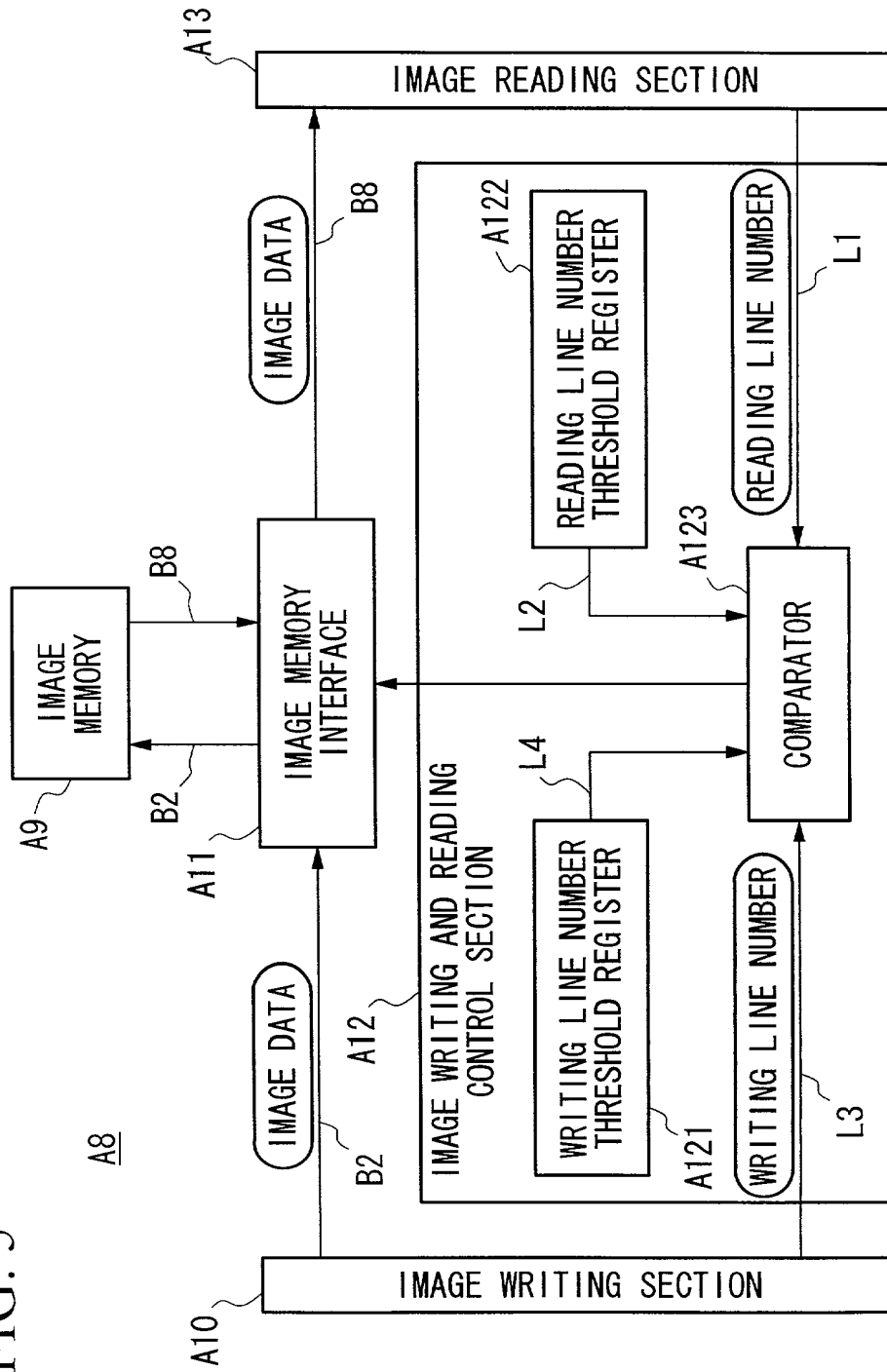


FIG. 6A

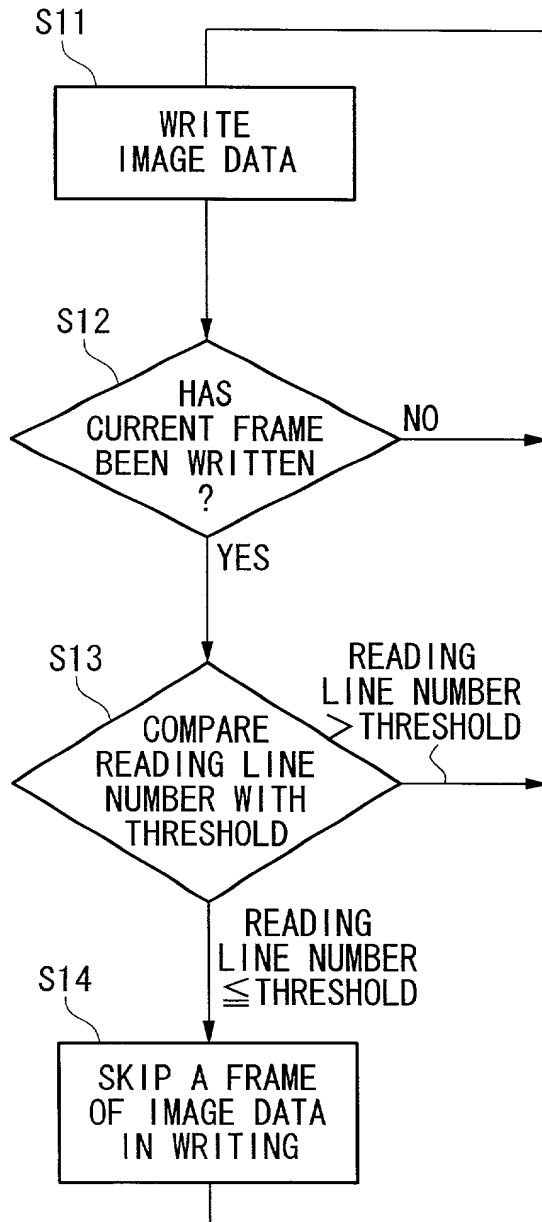


FIG. 6B

